



BIO-DATA

1.	Name	:	VIVEK SINGH
2.	Father's name	:	MAHENDRA PRATAP SINGH
3.	Date of birth	:	10.02.1987
4.	Marital Status	:	Married
5.	Address	:	209/13 Rasulabad, Allahabad, Uttar Pradesh, India, Pin-211004
6.	Email & Mobile No.	:	vivek.10singh@gmail.com , +91-9455007693
	Appointments	:	
	Present	:	Assistant Professor in the Department of Electronics and Communication, SIET, Allahabad affiliated from AKTU (college code-162)

4. Educational Qualifications

Exam passed	University	Year of Passing	Subject /Area	Grades
D. Phil	University of Allahabad (Department of Electronics and Communication)	Pursuing (<i>Pre-submission completed</i>)	Modeling and Simulation for Analysis, Design and Optimization of Radio Frequency Devices and Circuits	
M. Tech.	University of Allahabad (Department of Electronics and	2012	Electronics Engineering	First Division 72.27%

	Communication)			
B. Tech.	Uttar Pradesh Technical University	2009	Electronics and Communications	First Division 70.24%
Intermediate	U.P. Board	2004	Science (Physics, Chemistry, Mathematics)	First Division 70.20%
High School	U.P. Board	2002	Hindi, English, Science, Social Science, Mathematics, Drawing	Second Division 52.17%
5. Other Academic Achievements				
Qualified UGC-NET National Eligibility Test for Assistant Professor in 2012 and 2013				
Qualified GATE 2010 Examination				

6. Teaching & Research Experience : About 6 years

7. List of Publications

Journal: (SCI & SCIE Indexed)

1. **Vivek Singh**, B. Mishra, P. N. Tripathi, and R. Singh, "A compact quad-band microstrip antenna for S and C-band applications," *Microwave and Optical Technology Letters.*, vol. 58, no. 6, pp. 1365–1369, Jun. 2016.
2. **Vivek Singh**, B. Mishra, A. K. Dwivedi and R. Singh, "Inverted L-Notch Loaded Hexa Band Circular Patch Antenna for X, Ku/K Band Applications" *Microwave and Optical Technology Letters.*, vol. 60, no. 8, pp. 2081–2088, 2018.
3. B. Mishra, **Vivek Singh**, and R. Singh, "Dual and wide-band slot loaded stacked microstrip patch antenna for WLAN/WiMAX applications," *Microsystem Technologies.*, vol. 23, no. 8, pp. 3467–3475, Aug. 2017.

4. B. Mishra, **Vivek Singh**, R. K. Singh, N. Singh and R. Singh, “A compact UWB patch antenna with defected ground for Ku/K band applications,” *Microwave and Optical Technology Letters.*, vol. 60, no. 1, pp. 1-6, 2017.
5. B. Mishra, **Vivek Singh**, and R. Singh, “Gap Coupled Dual Band Petal Shape Patch Antenna for WLAN / WiMAX Applications”. *Advances in Electrical and Electronic Engineering (AEEE)*, vol. 16, no. 2, pp. 185–198, 2018.

Journal: (SCOPUS Indexed)

6. **Vivek Singh**, B. Mishra, A. K. Pandey, A. K. Patel, S. Yadav and R. Singh, “Triple band CPW fed monopole leaf shaped patch antenna,” *Int. J. Commun. Antenna Propag.*, vol. 7, no. 2, pp. 135–141, 2017.
7. B. Mishra, **Vivek Singh**, C. Jha, A. K. Pandey, and R. Singh, “Microwave band pass filter synthesis using coupled inductor for ISM band applications,” *Int. J. Applied Eng. Res.*, vol. 12, no. 11, pp. 2862–2867, 2017.

Journal: (UGC Indexed)

8. **Vivek Singh**, G. Chandra, R. Singh, “Slit Loaded Dual Wideband Microstrip Patch Antenna,” *International Journal of Advanced Computer Engineering and Communication Technology (IJACECT)*, vol. 4, no. 3, pp. 20-22, 2015.
9. **Vivek Singh**, Chandrabhan, G. Chandra, R. Singh, “Circular Ring Loaded Monopole Dual Band Patch Antenna,” *International Journal of Computer Application (IJCA)*, Vol. 6, Issue 1, pp. 106-108, 2016.
10. B. Mishra, **Vivek Singh**, A. Dwivedi, and R. Singh, “Dual band microstrip patch antenna with double-sided notch,” *Int. J. Computer Application.*, vol. 6, no. 1, pp. 106–108, 2016. (Journal no-64190 & Serial no-3)
11. Aditya Kumar Singh, Kamal Prakash Pandey, **Vivek Singh**, “Representation and Conversion of Line Coding using Mealy Sequential Network,” *International Journal of Emerging Trends in Engineering and Development*, vol. 1, Issue 6, pp. 76-81, 2016.

12. **Vivek Singh** and B. Mishra, “FPGA implementation of various lines coding technique for efficient transmission of digital data in communication,” *Int. J. of Res. in Engineering and Technology*, Vol. 3, Issue 4, pp. 60-63. 2014.

Book Chapter: (SCOPUS Indexed)

13. B. Mishra, **Vivek Singh**, A. K. Dwivedi, A. K. Pandey, A. Sarwar, and R. Singh, “Slots loaded multilayered circular patch antenna for Wi-Fi/WLAN applications,” *Computing and Network Sustainability*, *Lecture Notes in Networks and Systems* 12 (Springer), pp. 49–59, 2017. DOI 10.1007/978-981-10-3935-5_6 2017.
14. B. Mishra, **Vivek Singh**, and R. Singh, “Gap Coupled Swastika Shaped Patch Antenna for X and Ku-band Applications,” *Optical and Wireless Technologies*, *Lecture Notes in Electrical Engineering* (Springer), 2017, ISBN: 978-9811073946 (In press).

Conference Proceedings: Full Papers in IEEE

15. **Vivek Singh**, B. Mishra, and R. Singh, “A compact and wide band microstrip patch antenna for X-band applications,” in 2015 Second International Conference on Advances in Computing and Communication Engineering, 2015, pp. 296–300.
16. B. Mishra, **Vivek Singh**, and R. Singh, “Design, analysis and simulation of microwave coupler,” in 2015 International Conference on Signal Processing and Communication (ICSC), 2015, pp. 370–373.
17. Chandrabhan, **Vivek Singh**, B. Mishra, R. Singh, “A Dual band Double Inverted L-Slots Microstrip Patch Antenna,” *Second International Conference on Advances in Computing and Communication Engineering(ICACCE)*, 2015, pp. 88-89.

Communicated Papers: (SCI Indexed)

18. **Vivek Singh**, B. Mishra and R. Singh, “Anchor Shape Gap Coupled Patch Antenna for WiMAX and WLAN Applications” *International Journal for Computation and Mathematics in Electrical and Electronic Engineering (COMPEL)*.

19. **Vivek Singh**, B. Mishra, R. Singh, “Dual-wideband Semi-circular Patch Antenna for Ku/K Band Applications,” Microwave and Optical Technology Letters.
20. B. Mishra, **Vivek Singh** and R. Singh, “A comprehensive review of English alphabet shaped patch antennas: Design of English alphabet H-shape gap coupled antenna for wireless applications.” Proceedings of the National Academy of Sciences, India Section A: Physical Sciences.

8. List of Seminars/ Conference/ Workshops/ Training programmes, attended.

- [1]. International Conference on Signal Processing and Communication 2015 (Sponsored by IEEE).
- [2]. International Conference on Advances in Computing and Communication Engineering (Sponsored by) IEEE Computer Society).
- [3]. International Conference on Recent Trends in Engineering and Material Sciences (ICEMS -2016), (Sponsored By) Elsevier.
- [4]. Program coordinator of a National Seminar on Wireless Communication System Design (WCSD-2016) in association with IETE at SIET, Allahabad.
- [5]. Embedded system training program of duration 4 months.
- [6]. National Workshop on Electronic system Design and Manufacturing at IIIT Allahabad.
- [7]. 6 weeks vocational training in Broad band System at Broadband Switch room CTO, Allahabad.
- [8]. VHDL training program at CETPA Infotech Pvt. Ltd.
- [9]. Workshop on Multiphysics Simulation Using COMSOL organized by MNNIT Allahabad.

[10]. Participated in NAPSTER software coding competition at IILM Academy of Higher Learning.

9. Area of Interest

Analog Communication, Digital Communication, Digital Logic Design, Wireless Communication.

10. Software Skills

Ansys HFSS v13, AWR Microwave Office Suit, Origin Lab, Smart Draw, Mendeley Desktop. Xilinx Simulation Tool.

11. Practical Knowledge

Experiments related with basic communication labs (Modulation, Line coding), Microwave Communication Labs, Measurement of antenna by vector network analyzer (VNA), VHDL Lab.

(VIVEK SINGH)