

Mr. Kamal Mishra

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PROFESSIONAL SUMMARY:

Qualification:	M.Tech
Work Experience:	More than 20 years
Current Academic Designation:	Associate professor
Organization:	Shambhunath Institute of Engineering & Technology (Affiliated to AKTU, Lucknow) Apporved by AICTE, New Delhi
Specialization:	Control System, Electronic Circuits, Analog and Digital Electronics, Electromagnetic Field Theory, VLSI.

CAREER HIGHLINGHTS:

❖ **TEACHING PROFILE:**

- Associate Professor in the Department of the Electronics & Communication,
“Shambhunath Institute of Engineering & Technology” from 2006. Teaching area:
- Control System
 - Electronic Circuits

JOB PROFILE:

- Supervising the students in preparing electronics project & summer training reports.
- Arranging workshops, corporate seminars, guest lecturers for the students.
- Providing personal counseling to the students related to their overall development and suggesting ways for their continuous improvement in the subject areas.
- 10 Years of teaching experience in coaching institutes.

RESPONSIBILITIES UNDERTAKEN:

- Taken Classes in Physics and Mathematics.
- Administration of coaching

EDUCATIONAL QUALIFICATION:

- M. Tech.(VLSI) AKTU Lucknow.
- B.E. (Electronics) form MMMEC Gorakhpur.
- Intermediate (June 1990) with 78.2% from UP Board.
- SSC (June 1988) with 80% from UP Board.

SHORT TERM COURSE/WORKSHOP/ CONFERENCE ATTENDED:

- Actively participated in workshop on Antenna & RF design for low power application conducted by IIIT Allahabad & IEEE (UP Section) conduct on Sept. 27-30-2012.

- Acting as program chairman of National Seminar on Mobile communication & VLSI design conducted by S.I.E.T Allahabad associated with ISTE (New Delhi) on 23 Jan 2010.
- Actively participated in workshop on “ADVANCEEE VLSI DESING AUTIOMATION” conducted by SHIATS Allahabad on 8th Sep. 2012.
- Actively participated in workshop on virtual instrumentation & its application sponsored by TEQIP, MNNIT & National Instruments India held on March 18/20/2009.
- Life Membership of Indian Society of Technical Education (New Delhi)

PUBLICATION:-Research Paper titled “High Performance Energy Efficient D Flip Flop Circuits in International Conference on Advances in Electrical,Power Control,Electronics and Communication Engineering(AEPCECE-2015) Organized By “KrishiSanskriti” on 6th and 7th June 2015
Venue:Jawaharlal Nehru University(J.N.U), New Delhi.

Personal Details:-

Father’s Name: -Shri Uma Shankar Mishra

Date of birth: - 10/07/1973

Mailing Address: - G-1/16 Kalindipuram Allahabad

Nationality: -Indian

Reference: -Dr.R.K Singh

Professor in EE department MNNIT Allahabad

Mobile No. 9936691653

Dr.Rajeev Srivastava

Professor in ME department MNNIT Allahabad

Mobile No. 9415014474

Mr.Gopesh Tiwari IIT Kanpur, Mobile no:-9956713311

I hereby declare that the above information given is true and correct to the best of my Knowledge.

Date:-

Place:-Allahabad

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